

| | | | | |
|-----------------------------------|---------------------------------------|--|--|-------------|
| Notice of References Cited | Application/Control No. 10/531,510 | | Applicant(s)/Patent Under Reexamination REGNIER, LAURENT | |
| | Examiner AFSAR M. QURESHI | | Art Unit 2616 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|--------------------|----------------|
| * | A | US-6,467,083 | 10-2002 | Yamashita, Fumiaki | 717/128 |
| | B | US- | | | |
| | C | US- | | | |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) | | | |
|---|---|---|--|--|--|
| * | U | THE NEXUS 5001 FORUM: "Standard for a Global Embedded Processor Debug Interface", 12/15/1999, IEEE-ISTO,XP002247195 | | | |
| | V | | | | |
| | W | | | | |
| | X | | | | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.